



INDIAN INSTITUTE OF TECHNOLOGY
KHARAGPUR – 721 302, INDIA
Sponsored Research and Industrial Consultancy

Mr. Rajarshi Banerjee
Administrative Officer (Projects)

ADVERTISEMENT NO. : IIT/SRIC/R/FEQR/2012/92, DATED 8th May, 2012

Applications are invited on plain paper for the following assignment in a purely time bound research project undertaken in the Dept of Computer Science and Engineering of this institute. The project will give the student the opportunity to work under the Formal Verification Group, IIT Kharagpur (<http://facweb.iitkgp.ernet.in/~pallab/forverif.html>) on state of the art problems in design verification in collaboration with SRC – a global consortium of semiconductor companies.

1. Name of the temporary assignment : Research Consultant - 1 Post
2. Name of the temporary research project : Formal Equivalence and Simulation Relations for AMS Behavioral Models (FEQR)
3. Name of the sponsoring Agency : Semiconductor Research Corporation (SRC), Research Triangle Park, NC 27709, USA
4. Consolidated Fellowship/ Compensation : Upto Rs. 25,000/- p.m (depending on qualification and experience)
5. Essential Qualifications & Experience : BTech / MTech in Computer Science / Electronics / Electrical Engineering with outstanding academic record. Proficiency in programming is mandatory.

Interested eligible persons may apply on plain paper, giving full bio-data along with attested copies of testimonials and a Demand Draft for Rs.50/- (Not for female candidates) drawn in favour of IIT Kharagpur payable at Kharagpur to the undersigned **on or before 15th June, 2012.**


(Rajarshi Banerjee)

Copy to:

1. Head of the Deptts. / Centres / Schools, IIT Kharagpur
2. Dr. Pallab Dasgupta, PI of above project, Dept. of Computer Science and Engineering
3. Prof-in-Charge, Institute Information Cell, IIT, Kharagpur
4. All Wardens, Halls of Residence, IIT Kharagpur.
5. All other IITs/ITBHU/Universities in Eastern Region
6. Notice Board (SRIC)
7. Advertisement file.
8. Project file